

Welcome to Intellitech *Links*TM

Links? Everything we do is linked in some way. We link scan-chains on our boards and in our ICs. SERDES connections are linked and require a training sequence to 'link up'. We may be professionally 'linked-in'. However, the most important links are our link to you the customer and the engineering community. I hope you enjoy this first issue and give us feedback at:

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Cindy Kim, Editor

"World famous in ...?"



I like Mel Brooks' movies. He makes me laugh. There's a quote in Mr. Brook's movie "*To Be or Not to Be*", I've remembered for some time. Anna Bronski, (played by Anne Bancroft), says to Professor Siletski (played by

Mr. Brooks) "You see, my husband is that great Polish actor, Frederick Bronski." "Who?" the Professor asks. With disappointment and surprise Bronski answers with the silly oxymoron "He's world-famous... in Poland."

JTAG vendors make me laugh too. Just recently I read "World's first JTAG tool with an open source language" touting a vendor's introduction of the Python scripting language. Well it's not exactly the first as Intellitech introduced the open source Tcl/Tk into our toolset back in 1997. What they really mean is they are "World's first" in their locale or first in the eyes of those who don't know about the other firsts. The vendor who made the announcement last year isn't participating in the development of IEEE P1687. Apparently they are unaware that Tcl will be used for on-chip DFX and instrument development in this standard. (Tcl is a *de facto* standard in many EDA tools). I see that often, short-vision vendors leading their customers down the wrong path, costing the customer more money with each wrong turn.

I recently did an internet search and found "World's first remote JTAG over Ethernet" touted by another vendor in their press release. Anyone who used Intellitech's remote JTAG training board with webcam back in 1997 knows that it was all remote and all TCP/IP based. Firmware from Intellitech enabled all of Sun Microsystems' servers to be JTAG testable over TCP/IP during that time period too. Sound interesting? You can get the same remote JTAG test, debug and programming) over Ethernet with Intellitech's NEBULA product. And what's more interesting is that it is FREE. (Read more of this issue of *Links* to find out how to freely download NEBULA for silicon instruments).

Are you the creative type? And perhaps you can use an extra \$1,000.00? Enter Intellitech's contest for the best NEBULA instrument application (read more in this issue of *Links*.)

Thanks for reading *Links*, my goal is to give you information you can trust. No marketing hype or distortion of history here.

Cindy Kim, Editor

"From the CEO"

by CJ Clark



It is with great pleasure that I have the opportunity in this forum to communicate with friends and customers of Intellitech. Your continued embrace of Intellitech's technology motivates us to further innovate.

A decade ago, we launched our TEST-IP family of embedded in-chip and in-FPGA IP at a time when vendors were focused on being boundary-scan companies. Early conference presentations, while rudimentary by today's standards, focused on designing ICs not only for testing on ATE but also to be a 'good neighbor' when it is part of a system ([Read more](#)). Any IC which has value ends up part of a system. Many on-chip functions are needed to help the end-user (read end-customer) long after the IC leaves the production floor. Monitors, BISR's, Debug triggers,